



# G. H. RAISONI COLLEGE OF ENGINEERING, NAGPUR.

(An Autonomous Institution under UGC Act, 1956)

C.R.P.F. Gate No. 3, Hingna Road, Digidoh Hills, Nagpur, Maharashtra 440 016 (India)

## M. TECH. / M. E. FIRST SEMESTER END SEMESTER EXAMINATION OF WINTER - 2015

=: PROGRAMME (WRITTEN):=

TIME:-2.00 P.M. To 5.00 P.M.

(Old Scheme 2010-11)

(Autonomous)

| Day / Date                     | CAD / CAM                         | Heat Power Engineering        | Integrated Power System    | VLSI                               | Electronics Engineering            | Communication Engineering                     | Computer Science Engineering           | Embedded System & Comp.                    | Wireless Comm. & Comp.           | Mobile Technology               |
|--------------------------------|-----------------------------------|-------------------------------|----------------------------|------------------------------------|------------------------------------|---|--|--|----------------------------------|---------------------------------|
| <b>Tuesday</b><br>27/10/2015   | CNC & Robotics                    | Advanced Thermodynamics       | Advanced Control Theory    | VLSI Sub-System Design             | VLSI Circuits                      | Error Control Coding                          | Network Security & Cryptography        | Embedded System Concepts                   | <b>Elective - I *</b>            | Network Security & Cryptography |
| <b>Thursday</b><br>29/10/2015  | Computer Integrated Manufacturing | Thermal Engineering-I         | Advanced Power Electronics | Switching Theory & Automata        | Soft Computing & Applications      | Wireless Communication & Broadband Technology | Soft Computing                         | Soft Computing                             | Distributed Computing            | Wireless & Mobile Communication |
| <b>Saturday</b><br>31/10/2015  | Computer Graphics for CAD / CAM   | Heat Transfer-I               | HVDC Transmission systems  | Advanced Digital Signal Processing | Advanced Digital Signal Processing | Advanced Digital Communication                | <b>Elective - I \$</b>                 | Microcontroller for Embedded System Design | Wireless Communication Networks  | GSM Network                     |
| <b>Monday</b><br>02/11/2015    | <b>Elective - I ^</b>             | Advanced Energy Technology    | Switchgear and Protection  | Modeling of Digital System-I       | Data Communications                | DSP Processors and Architecture               | Distributed Operating System           | <b>Elective - I @</b>                      | Algorithms for Wireless Networks | IP Telephony                    |
| <b>Wednesday</b><br>04/11/2015 | Data Structures and Algorithm     | Computer Aided Design & Engg. | Power System Modeling      | Embedded System-I                  | Embedded System-I                  | <b>Elective - I ▲</b>                         | High Performance Computer Architecture | High Performance Computer Architecture     | Signaling & Communication        | Signaling & Communication       |

### **Elective - I ^**

1. Materials Engineering

2. Image Processing

3. Engineering Management

4. Total Quality System & Engineering

### **Elective - I ▲**

1. DIP and Wavelet Signal Processing

2. Verilog HDL

3. High Speed Communication Networks

### **Elective - I \$**

1. Pervasive Computing

2. Artificial Intelligence & Expert Systems

### **Elective - I @**

1. VLSI Technology and Design

2. Modeling of Digital Systems

### **Elective - I \***

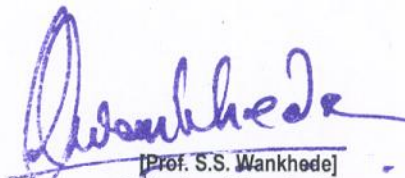
1. Satellite Communication

2. Networking Protocol

Date of answerbook showing to the students in respective department will be as under:-

04/11/2015 :- The paper of the date 27/10/2015, 29/10/2015, 31/10/2015

06/11/2015 :- The paper of the date 02/11/2015, 04/11/2015



[Prof. S.S. Wankhede]  
Controller of Examinations

G. H. Raisoni College of Engineering, Nagpur