

**G.H. RAISONI COLLEGE OF ENGINEERING**

(An Autonomous Institute affiliated to Rashtrasant Tukdoji Maharaj Nagpur University Nagpur)

(Accredited by NACC 'A' Grade and National Board of Accreditation, Approved by AICTE)

CRPF Gate No.3, Hingna Road, Digdoh Hills, Nagpur-440016 (INDIA)

**DEPARTMENT OF ELECTRONICS ENGINEERING**

**Time Table - Telecom Embedded Hardware Developer Course under PMKVY**

**BATCH-II**

W.e.f. Date : 01/12/2017

Session: 2017-18

ROOM NO:E17

DAYS	1	2	3	4	5	6	7	8
	9.30 to 10.30	10.30 to 11.30	11.30 to 12.30	12.30 to 1.30	1.30 to 2	2 to 3	3 to 4	4 to 5
MON								
TUE								
WED								
THU								
FRI	CD	CD	GRT	GRT	LUNCH BREAK	MMP	SNJ	SNJ
SAT	CD	GRT	GRT	SSD	LUNCH BREAK	MMP	SVB	SVB

**Dr.S.S.Dorle**

**Prof.S.N.Joshi**

**Prof.S.V.Bhalerao**

**Prof.M.M.Pathan**

**Prof. Chetan Dhule**

**Prof.R.S.Agrawal**  
Course Coordinator

**Prof. Girish Talamale**  
Faculty

**Dr.S.S.Dorle**  
Head of Department

**Dr.S.S.Salankar**  
Dean Students

**Dr. P.R. Bajaj**  
Director