



G H RAISONI COLLEGE OF ENGINEERING NAGPUR

(An Autonomous Institute affiliated to Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur)

CRPF Gate No.3, Hingna Road, Digdoh Hills, Nagpur. Maharashtra-440016 (India)

Course wise Teaching Plan for Session : Summer Term 2018

Course : Computer Architecture & Organization

Faculty : Idole - Lalit Dole

Unit	Topic Code	Topic Covered	Date	Course	Section
1	1	Functional Units	03/05/2018	BCSL202	A
1	2	Basic Operational Concepts	03/05/2018	BCSL202	A
1	3	Bus Structures	04/05/2018	BCSL202	A
1	4	Addressing Modes	04/05/2018	BCSL202	A
1	5	Subroutines: Parameter Passing	05/05/2018	BCSL202	A
1	6	Instruction Formats	05/05/2018	BCSL202	A
1	7	Expanding Opcodes Method	08/05/2018	BCSL202	A
2	8	Bus Architecture	08/05/2018	BCSL202	A
2	9	Execution of a Complete Instruction	08/05/2018	BCSL202	A
2	10	Sequencing of Control Signals	10/05/2018	BCSL202	A
2	11	Hardwired Control	10/05/2018	BCSL202	A
2	12	Microprogrammed Control	11/05/2018	BCSL202	A
2	13	Microinstruction Format	11/05/2018	BCSL202	A
2	14	Bit Slice Concept	12/05/2018	BCSL202	A
3	15	Number Representations and their Operations	15/05/2018	BCSL202	A
3	16	Design of Fast Adders	15/05/2018	BCSL202	A
3	17	Signed Multiplication	15/05/2018	BCSL202	A
3	18	Booth's Algorithm, Bit-Pair Recoding	17/05/2018	BCSL202	A
3	19	bit-pair recoding	17/05/2018	BCSL202	A
3	20	Integer Division	18/05/2018	BCSL202	A
3	21	Floating Point Numbers and Operations	18/05/2018	BCSL202	A
3	22	Guard Bits and Rounding	19/05/2018	BCSL202	A



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4	23	Various Technologies used in Memory Design	22/05/2018	BCSL202	A
4	24	Higher Order Memory Design	22/05/2018	BCSL202	A
4	25	Multimodal Memories and Interleaving	22/05/2018	BCSL202	A
4	26	Associative Memory	24/05/2018	BCSL202	A
4	27	Cache Memory	24/05/2018	BCSL202	A
5	28	Synchronous vs. Asynchronous Data Transfer	25/05/2018	BCSL202	A
5	29	Direct Memory Access	25/05/2018	BCSL202	A
5	30	COMPUTER PERIPHERALS: I/O devices such as Magnetic Disk, Magnetic Tape, CDROM Systems.	26/05/2018	BCSL202	A
6	31	Pipe lining..	29/05/2018	BCSL202	A
6	32	Delayed Branch	29/05/2018	BCSL202	A
6	33	Branch Prediction	29/05/2018	BCSL202	A
6	34	Data Dependency	31/05/2018	BCSL202	A
6	35	Data Dependency	01/06/2018	BCSL202	A
6	36	Influence of Pipelining on Instruction Set Design	01/06/2018	BCSL202	A
6	37	Influence of Pipelining on Instruction Set Design	02/06/2018	BCSL202	A
6	38	Multiple Execution Units, Performance Considerations	05/06/2018	BCSL202	A
6	39	Multiple Execution Units, Performance Considerations	05/06/2018	BCSL202	A
6	40	Basic concepts in Parallel Processing & Classification of Parallel Architectures	05/06/2018	BCSL202	A
6	41	Basic concepts in Parallel Processing & Classification of Parallel Architectures	07/06/2018	BCSL202	A
6	42	Vector Processing, Array Processors	07/06/2018	BCSL202	A
6	43	Recent trends in Computer Architecture & Organization	08/06/2018	BCSL202	A



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6	44	Advanced topics & its Applications	08/06/2018	BCSL202	A
6	45	Revision	09/06/2018	BCSL202	A